Rev. A2, Mar.27 2019

Document NO.: MT-E-PS-Q0460 Security Classification: General

# SGE10-D0C-T1

#### **Features**

- ◆ Up to 1.25 Gbps data rate
- ◆ 1310nm FP Laser and PIN photo detector
- ◆ Duplex LC receptacle optical interface compliant
- ◆ Single +3.3V power supply
- ♦ Hot-pluggable
- ◆ AC coupling of LVPECL signals
- ◆ International Class1 laser safety certified
- ◆ Operating temperature range: Commercial:0°C~+70°C
- ◆ RoHS Compliant
- ◆ DDMI function available with internally calibrated mode

#### **Applications**

- ◆ Gigabit Ethernet
- Gigabit Fiber Channel
- ◆ Switch to switch interface
- ◆ Switched backplane applications

#### **Standards**

- ◆ Compliant with SFP MSA (INF-8074i)
- ◆ Compliant with SFF-8472
- ◆ Compatible with IEEE802.3ah 2004

Page 1 of 11 FORM NO.: FORM-MT-0539REV.05



# 1.25Gbps 10km LC Duplex SFP Transceiver Rev. A2, Mar.27 2019

Document NO.: MT-E-PS-Q0460 Security Classification: General

# **Specification**

Absolute Maximum Ratings					
Parameter	Symbol	Min	Max	Unit	
Storage temperature	TS	-40	85	$^{\circ}$	
Power Supply Voltage	Vcc	-0.5	+4	V	
Relative Humidity	RH	5	95	%	

Recommended Operating Conditions					
Parameter	Max	Unit			
Operating Case Temperature	Tc	0		70	$^{\circ}$
Power Supply Voltage	Vcc	3.13	3.3	3.47	V
Supply Current <sup>1</sup>	Icc			300	mA
Data Rate			1.25		Gbps
Fiber Length 9/125µm core SMF				10	km

Electrical Characteristics						
Parameter	Symbol	Min	Typical	Max	Unit	Notes
Transmitter differential input voltage		400		2400	mV	
Receiver differential output Voltage		600		1200	mV	
T	Voh	2.4		Vcc+0.3	V	LVTTL
Transmit Fault (TX_Fault)	Vol	-0.3		0.4	V	LVTTL
1 (0: 1/1.00)	Voh	2.4		Vcc+0.3	V	LVTTL
Loss of Signal (LOS)	Vol	-0.3		0.4	V	LVTTL
TX Disable	Vih	2		Vcc+0.3	V	LVTTL
I A DISABle	Vil	-0.3		0.8	V	LVTTL

Optical transmitter Characteristics						
Parameter Symbol Min Typical Max Unit Notes						Notes
Launched Power (avg.)	Pout	-9		-3	dBm	
Operating Wavelength Range	λс	1270	1310	1355	nm	
Spectral Width (RMS)	Δλ			4	nm	FP

FORM NO.: FORM-MT-0539REV.05 Page 2 of 11



Rev. A2, Mar.27 2019

Document NO.: MT-E-PS-Q0460 Security Classification: General

Extinction Ratio		ER	9			dB	2
Relative Intensity Noise		RIN			-120	dB/Hz	
	Return Loss		12			dB	
Optio	cal Rise/Fall Time	Tris/Tfall			260	PS	3
Transm	nitter and Dispersion Penalty	TDP			1	dB	
Optica	al Tx Output disable	P <sub>dis</sub>			-45	dBm	
Outp	Output Eye Diagram Complies with IEEE802.3z eye masks when filtered					filtered	
		Optica	l receiver	Character	istics		
	Parameter	Symbol	Min	Typical	Max	Unit	Notes
Red	Receiver Sensitivity					JD	
Wavelength Range		S			-23	dBm	4
Wa	•	λc	1260		-23 1610	nm	4
	•		1260				4
Rec	velength Range		1260		1610	nm	4
Rec Optical I	eiver Reflectance	λς			1610	nm dB dBm	4
Rec	eiver Reflectance Power Input Overload	λc P <sub>in-max</sub>			1610 -12	nm dB	

Note1. The supply current is SFP module's working current.

**Note2**: For the measurements, the device was driven with 1.25Gbps data pattern with 2<sup>7</sup>-1 PRBS payload

**Note3**. Optical transition time is the time interval required for the rising or falling edge of an optical pulse to transition between the 20% and 80% amplitudes relative to the logical 1 and 0 levels

Note4. Measured with a PRBS 2<sup>7</sup>-1 test pattern, @1.25Gbps, ER=10dB, BER<10<sup>-12</sup>

**Note5**. The LOS Hysteresis minimizes 'chatter' on the output line. In principle, Hysteresis alone does not guarantee chatter-free operation.

### **Digital Diagnostic Monitoring Information**

Parameter	Accuracy	Calibration	Note
Temperature	<b>±3</b> ℃	internal	0~70℃
Voltage	±3%	internal	3.13V~3.47V
Bias Current	±10%	internal	Specified by normal value
TX Power	±2dB	internal	-9~-3dBm
RX Power	±2dB	internal	-23~-3dBm

Page 3 of 11 FORM NO.: FORM-MT-0539REV.05



Rev. A2, Mar.27 2019

Document NO.: MT-E-PS-Q0460 Security Classification: General

#### Pin definition

20	V. T
20	VeeT
19	TD-
18	TD+
17	VeeT
16	VecT
15	VecR
14	VeeR
13	RD+
12	RD-
11	VeeR

1	VeeT
2	Tx_Fault
3	Tx disable
	MOD-DEF(2)
5	MOD-DEF(1)
6	MOD-DEF(0)
7	Rate Select
8	LOS
9	VeeR
10	VeeR

Top of Board Bottom of Board

As Viewed Through Top of Board

Pin	Name	Function/Description	Engage-ment	Order
1	VeeT	Transmitter Ground	1	
2	TX Fault	Transmitter Fault Indication	3	1
3	TX Disable	Transmitter Disable-Module disables on high or open	3	2
4	MOD-DEF2	Module Definition 2-Two wire serial ID interface	3	3
5	MOD-DEF1	Module Definition 1-Two wire serial ID interface	3	3
6	MOD-DEF0	Module Definition 0-Two wire serial ID interface	3	3
7	Rate Select	Not Connected	3	
8	LOS	Loss of Signal	3	4
9	VeeR	Receiver Ground	1	
10	VeeR	Receiver Ground	1	
11	VeeR	Receiver Ground	1	
12	RD-	Inverse Received Data out	3	5
13	RD+	Received Data out	3	5
14	VeeR	Receiver Ground	1	
15	VccR	Receiver Power —— +3.3V±5%	2	6
16	VccT	Transmitter Power —— +3.3 V±5%	2	6
17	VeeT	Transmitter Ground	1	

Page 4 of 11 FORM NO.: FORM-MT-0539REV.05



Rev. A2, Mar.27 2019

Document NO.: MT-E-PS-Q0460 Security Classification: General

18	TD+	Transmitter Data In	3	7
19	TD-	Inverse Transmitter Data In	3	7
20	VeeT	Transmitter Ground	1	

**Note1**. TX Fault is open collector/drain output which should be pulled up externally with a  $4.7K - 10K\Omega$  resistor on the host board to supply <VccT+0.3V or VccR+0.3V. When high, this output indicates a laser fault of some kind. Low indicates normal operation. In the low state, the output will be pulled to <0.8V.

**Note2**. TX Disable input is used to shut down the laser output per the state table below. It is pulled up within the module with a 4.7K – 10K Resistor.

Low (0 - 0.8V): Transmitter on;

Between (0.8V and 2V): Undefined High;

(2.0 – VccT): Transmitter Disabled;

Open: Transmitter Disabled.

**Note3**. Mod-Def 0, 1, 2. These are the module definition pins. They should be pulled up with a 4.7 – 10K Resistor on the host board to supply less than VccT+0.3V or VccR+0.3V.

Mod-Def 0 is grounded by the module to indicate that the module is present.

Mod-Def 1 is clock line of two wire serial interface for optional serial ID.

Mod-Def 2 is data line of two wire serial interface for optional serial ID.

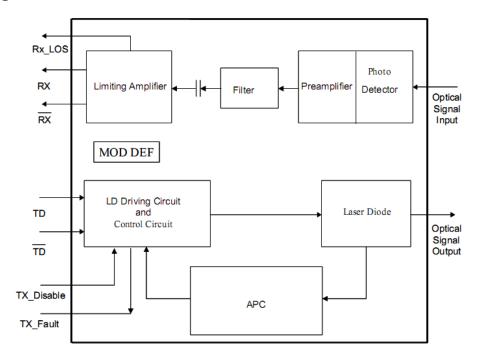
- **Note4**. LOS (Loss of signal) is an open collector/drain output which should be pulled up externally with a 4.7 10K resistor on the host board to supply <VccT+0.3V or VccR+0.3V. When high, this output indicates the received optical power is below the worst case receiver sensitivity (as defined by the standard in use). Low indicates normal operation. In the low state, the output will be pulled to <0.8V.
- **Note5**. RD-/+: These are the differential receiver outputs. They are AC coupled  $100\Omega$  differential lines which should be terminated with  $100\Omega$  differential at the user SERDES. The AC coupling is done inside the module and thus not required on the host board.
- **Note6**. VccR and VccT are the receiver and transmitter power supplies. They are defined as 3.3V±5% at the SFP connector pin. The in-rush current will typically be no more than 30mA above steady state supply current after 500ns.
- **Note7.** TD-/+: These are the differential transmitter inputs. They are AC coupled differential lines with  $100\Omega$  differential termination inside the module. The AC coupling is done inside the module and is thus not required on host board.

Page 5 of 11 FORM NO.: FORM-MT-0539REV.05

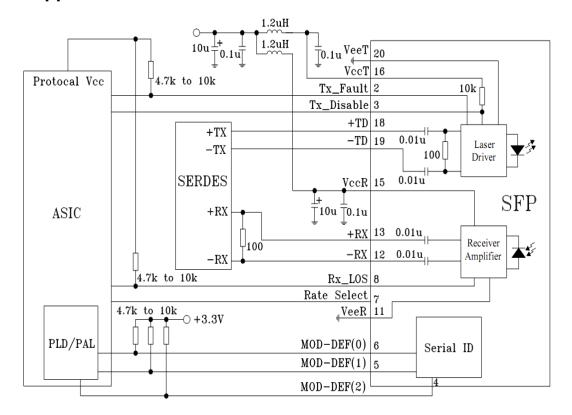
Rev. A2, Mar.27 2019

Document NO.: MT-E-PS-Q0460 Security Classification: General

#### **Block Diagram**



#### **Typical application Circuit**

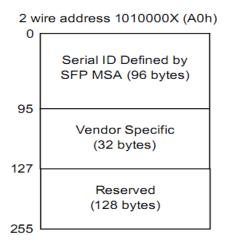


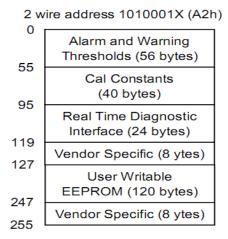
Page 6 of 11 FORM NO.: FORM-MT-0539REV.05

Rev. A2, Mar.27 2019

Document NO.: MT-E-PS-Q0460 Security Classification: General

#### **EEPROM Memory Map**





#### **EEPROM Serial ID Memory Contents**

The optical transceiver contains an EEPROM. It provides access to sophisticated identification information that describes the transceiver's capabilities, standard interfaces, manufacturer, and other information. When the serial protocol is activated, the host generates the serial clock signal (SCL, Mod Def 1). The positive edge clocks data into those segments of the EEPROM that are not writing protected within the SFP transceiver. The negative edge clocks data from the SFP transceiver. The serial data signal (SDA, Mod Def 2) is bi-directional for serial data transfer. The host uses SDA in conjunction with SCL to mark the start and end of serial protocol activation. The memories are organized as a series of 8-bit data words that can be addressed individually or sequentially.

The Module provides diagnostic information about the present operating conditions. The transceiver generates this diagnostic data by digitization of internal analog signals. Calibration and alarm/warning threshold data is written during device manufacture. Received power monitoring, transmitted power monitoring, bias current monitoring, supply voltage monitoring and temperature monitoring all are implemented. The diagnostic data are raw A/D values and must be converted to real world units using calibration constants stored in EEPROM locations 56 – 95 at wire serial bus address A2h. The digital diagnostic memory map specific data fields define as following.

#### **EEPROM Serial ID Memory Contents (2-Wire Address A0h)**

Address	Name of field	Hex	Description
		BASE ID Fields	
00	Identifier	03	SFP transceiver
01	Ext. Identifier	04	Serial ID module supported for SFP
02	Connector	07	LC
03-10	Transceiver Codes		

Page 7 of 11 FORM NO.: FORM-MT-0539REV.05



# 1.25Gbps 10km LC Duplex SFP Transceiver Rev. A2, Mar.27 2019

Document NO.: MT-E-PS-Q0460 Security Classification: General

Security Classification. General						
11	Encoding	01	Encoding codes			
12	BR, Nominal	0D				
13	Rate Identifier	00	Not defined			
14	Length(9um)-km	0A				
15	Length(9um)-m	64				
16	Length(50um)	00	Transceiver transmit distance			
17	Length(62.5um)	00	Transceiver transmit distance			
18	Length(cable)	00				
19	Length(OM3)	00				
20-35	Vendor Name	4D 45 4E 54 45 43 48 4F 50 54 4F	"MENTECHOPTO"(ASCII character)			
36	Reserved	00	Not defined			
37-39	Vendor OUI	00 00 00	Not defined			
40-55	Vendor P/N					
56-59	Vendor P/N Rev.	31 2E 30 20	"1.0"(ASCII character)			
60-61	Laser Wavelength	05 1E	1310nm			
62	Reserved	00	Not defined			
63	CC_BASE	XX	Check sum of bytes 0-62			
		Extended ID Fields				
64-65	Options	00 1A	RX_LOS、TX_Faultare implemented			
66	BR, max	14	Upper bit rate margin,20%			
67	BR, min	14	Lower bit rate margin,20%			
68-83	Vendor SN		Vendor Serial Number in ASCII character			
84-91	Date Code	Data Code	Vendor Date Code in ASCII character			
92	Diagnostic Monitoring Type	68	Digital Diagnostic monitoring implemented "Internally calibrated" is implemented, RX measurement type is "Average Power"			
93	Enhanced options	F0	Optional Alarm/warning flags, soft Tx_Fault monitoring, soft LOS monitoring are implemented			
94	SFF-8472 compliant	05	SFF-8472 compliant with revision 11.0			
95	CC-EXT	XX	Check sum of bytes 64-94			
		Vendor Specific ID Field	d			
96-127	Vendor Specific	00	Vendor specific EEPROM			
128-255	Reserved	00	Reserved for future use			
-	·					

FORM NO.: FORM-MT-0539REV.05 Page 8 of 11



Rev. A2, Mar.27 2019

Document NO.: MT-E-PS-Q0460 Security Classification: General

# Digital Diagnostic Monitoring Interface: Alarm and Warning Thresholds (2-Wire Address A2h)

Adduss	#D. 4	Nama	Real Value	11:4	
Address	#Bytes	Name	Commercial	Unit	Hex
00-01	2	Temp High Alarm	80	$^{\circ}\mathbb{C}$	
02-03	2	Temp Low Alarm	-10	$^{\circ}\mathbb{C}$	
04-05	2	Temp High Warning	70	$^{\circ}\mathbb{C}$	
06-07	2	Temp Low Warning	0	$^{\circ}\mathbb{C}$	
08-09	2	Voltage High Alarm	3.7	V	
10-11	2	Voltage Low Alarm	3.0	V	
12-13	2	Voltage High Warning	3.5	V	
14-15	2	Voltage Low Warning	ow Warning 3.1		
16-17	2	Bias High Alarm	70	mA	
18-19	2	Bias Low Alarm	1	mA	
20-21	2	Bias High Warning	65	mA	
22-23	2	Bias Low Warning	Bias Low Warning 1.5		
24-25	2	TX Power High Alarm	TX Power High Alarm -1		
26-27	2	TX Power Low Alarm	TX Power Low Alarm -11		
28-29	2	TX Power High Warning	TX Power High Warning -3		
30-31	2	TX Power Low Warning	-9	dBm	
32-33	2	RX Power High Alarm	-1	dBm	
34-35	2	RX Power Low Alarm	-25	dBm	
36-37	2	RX Power High Warning	-3	dBm	
38-39	2	RX Power Low Warning	-23	dBm	
40-55	16	Reserved	Reserved		

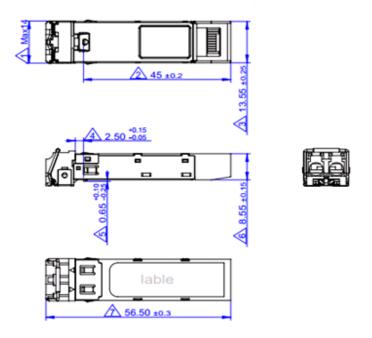
Page 9 of 11 FORM NO.: FORM-MT-0539REV.05

Rev. A2, Mar.27 2019

Document NO.: MT-E-PS-Q0460 Security Classification: General

#### **Package Outline**

All dimensions are ±0.1mm unless otherwise specified. (Unit: mm)



## **Regulatory Compliance**

Feature	Test	Method		
Electrostatic Discharge (ESD) to the Electrical Pins	MIL-STD-883E Method 3015.7	Class 1(>1000V for SFI pins, >2000Vfor other pins.)		
Electrostatic Discharge (ESD) Immunity	IEC61000-4-2	Class 2(>4.0kV)		
Electromagnetic Interference (EMI)	CISPR22 ITE Class B FCC Class B CENELEC EN55022 VCCI Class 1	Comply with standard		
Immunity	IEC61000-4-3	Comply with standard		
Eye Safety	FDA 21CFR 1040.10 and 1040.11 EN (IEC) 60825-1,2	Compatible with Class I laser Product		

Page 10 of 11 FORM NO.: FORM-MT-0539REV.05



# 1.25Gbps 10km LC Duplex SFP Transceiver Rev. A2, Mar.27 2019

Document NO.: MT-E-PS-Q0460 Security Classification: General

## **Ordering information**

Part. No	Specifications								
	Pack	Rate (Gbps)	Tx (nm)	Po (dBm)	RX	Sen (dBm)	Temp (℃)	Reach (km)	DDM
SGE10-D0C-T1	SFP	1.25	1310 FP	-9~-3	PIN	<-23	0~+70	10	Y

Page 11 of 11 FORM NO.: FORM-MT-0539REV.05